

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L2	25	(control\$4 with logic) with output with tri\$2state with low with high with (\$4enabl\$3 or disabl\$3 or "not enable")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/13 15:27
L3	38	(configuration or control\$4 with logic) with output with tri\$2state with low with high with (\$4enabl\$3 or disabl\$3 or "not enable")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/13 15:29
L4	3	(configuration with logic) with output with tri\$2state with low with high with (\$4enabl\$3 or disabl\$3 or "not enable")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/13 13:54
L5	26	((configuration or control\$4) with logic) with output with tri\$2state with low with high with (\$4enabl\$3 or disabl\$3 or "not enable")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/13 13:54
L7	1904	((tri\$2state and high and low) with (mode or state) with output) and (disable or ("not enable") or enable)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/13 15:21
L10	520	(configuration or (control\$4 with logic)) and ((tri\$2state with high with low) with (mode or state) with output) and (disable or ("not enable") or enable)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/13 15:28
L11	72	(configuration or (control\$4 with logic)) with ((tri\$2state with high with low) with (mode or state) with output) and (disable or ("not enable") or enable)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/13 14:26
L19	1	"375"/.cccls. and (control\$4 with logic) with output with tri\$2state with low with high with (\$4enabl\$3 or disabl\$3 or "not enable")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/13 15:18
L20	15	"375"/.cccls. and (configuration or (control\$4 with logic)) and ((tri\$2state with high with low) with (mode or state) with output) and (disable or ("not enable") or enable)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/13 15:19

L21	0	"375"/201.ccls. and (configuration or (control\$4 with logic)) and ((tri\$2state with high with low) with (mode or state) with output) and (disable or ("not enable") or enable)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/13 15:19
L22	2	"332"/.ccls. and ((tri\$2state and high and low) with (mode or state) with output) and (disable or ("not enable") or enable)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/13 15:21
L23	1	"332"/109.ccls. and ((tri\$2state and high and low) with (mode or state) with output) and (disable or ("not enable") or enable)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/13 15:21
L25	1	"332"/.ccls. and (PWM or pulse adj1 width adj1 modulat\$3) and ((control with logic) with tri\$1state)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/13 15:22
L26	1	"375"/.ccls. and (PWM or pulse adj1 width adj1 modulat\$3) and ((control with logic) with tri\$1state)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/13 15:22
L27	9	"326"/.ccls. and (control\$4 with logic) with output with tri\$2state with low with high with (\$4enabl\$3 or disabl\$3 or "not enable")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/13 15:28
L28	161	"326"/.ccls. and (configuration or (control\$4 with logic)) and ((tri\$2state with high with low) with (mode or state) with output) and (disable or ("not enable") or enable)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/13 15:28
L29	20	"326"/.ccls. and (configuration or control\$4 with logic) with output with tri\$2state with low with high with (\$4enabl\$3 or disabl\$3 or "not enable")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/13 15:29
L30	22	"326"/.ccls. and ((tri\$2state with bit) and (high with bit) and (low with bit)) with ("not enable" or disable)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/13 15:32
L31	13	"326"/.ccls. and (control\$4 with "any one of" same (tri\$2state and high and low) with (mode or state) with output with (transistor or FET)) and (disable or ("not enable") or enable)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/13 15:30

L32	1	"326"/.cccls. and Bowling with Stephen	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/13 15:32
L33	27	"326"/.cccls. and ("any one of" same (tri\$2state and high and low) with (mode or state) with output with (transistor or FET)) and (disable or ("not enable") or enable)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/13 15:33
S1	2	(configuration with bit\$1) with tri\$1state with active with (high and low) and (PWM or pulse adj1 width adj1 modulat\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/10 07:56
S12	2	(processor or flash or ROM or RAM) with (control with bit\$1 with logic) with tri\$1state with active with (high or low)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/09/15 13:52
S16	11	"6061780"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/09/15 08:00
S17	2	(configuration with bit\$1) with tri\$1state with active with (high and low)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/09/15 07:55
S21	5	"configuration bits" and (tri-state with active with (high and low))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/09/15 07:56
S25	16	"5056004"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/09/15 08:00
S26	6	"5056004" and configuration	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/09/15 08:19
S28	57	Bowling with Stephen	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/13 15:32
S30	34	(Bowling with Stephen) and (configuration\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/10 09:00
S31	7	(Bowling with Stephen) and PWM	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/09/15 08:22

S14 6	4	(PWM or pulse adj1 width adj1 modulat\$3) and ((control with logic) with tri\$1state with active with (high or low))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/10 09:01
S14 7	17	(PWM or pulse adj1 width adj1 modulat\$3) and (tri\$1state with active with (high or low))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/09/15 13:57
S14 8	27	(PWM or pulse adj1 width adj1 modulat\$3) and ((control with logic) with tri\$1state)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/13 15:21
S92 2	2	((configuration or control\$4) with bit\$1) with tri\$1state with active with (high and low) and (PWM or pulse adj1 width adj1 modulat\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/10 07:46
S92 3	3	((configuration or control\$4) with bit\$1) with tri\$1state with active with (high and low) and (transistor or FET) with pair	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/10 07:48
S92 4	11	"5506484"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/10 07:51
S92 5	0	"5506484" and tri\$2state	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/10 07:48
S92 6	0	"5506484" and tri-state	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/10 07:48
S92 7	0	"5506484" and tristate	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/10 07:48
S92 8	7	"5973527"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/10 07:51
S92 9	20	"6002234"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/10 08:00
S93 0	0	"6002234" and tri\$2state	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/10 07:56

S93 1	3	"6434020"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/10 08:01
S93 2	2	"6552625"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/10 08:02
S93 3	2	"6643150"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/10 08:04
S93 4	4	"6724169"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/10 08:14
S93 5	0	"6724169" and tri\$2state	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/10 08:06
S93 6	0	"6724169" and (tri-state or tristate)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/10 08:07
S93 7	1	"6201437".PN.	USPAT; USOCR	OR	ON	2005/06/10 08:12
S93 8	1	"6160735".PN.	USPAT; USOCR	OR	ON	2005/06/10 08:12
S93 9	1	"6005366".PN.	USPAT; USOCR	OR	ON	2005/06/10 08:13
S94 0	1	"5216587".PN.	USPAT; USOCR	OR	ON	2005/06/10 08:13
S94 1	2	"20040150439"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/10 08:15
S94 2	30	"5974549"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/10 08:15
S94 3	0	"5974549" and tri\$2state	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/10 08:15
S94 4	20	"6055619"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/10 08:16

S94 5	0	"6055619" and tri\$2state	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/10 08:16
S94 6	4	"6282637"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/10 08:17
S94 7	3	"6728856"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/10 08:17
S94 8	13	"5701493"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/10 08:18
S94 9	24	"5386563"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/10 08:18
S95 0	0	"5386563" and tri\$2state	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/10 08:18
S95 1	2	"6694398"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/10 08:18
S95 2	4	"5623646"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/10 08:20
S95 3	49	"4709324"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/10 08:20
S95 4	0	"4709324" and tri\$2state	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/10 08:20
S95 5	31	"4945507"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/10 08:20
S95 6	2	"4945507" and tri\$2state	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/10 08:21

S958	7	"3930253"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/10 08:22
S959	4	"5561384" and tri\$2state	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/10 08:24
S960	4	"5561384" and tri\$2state and (enable or disable)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/10 10:34
S961	4	"5561384" and tri\$2state and (enable or disable) and high and low	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/10 08:53
S962	0	"5561384" and tri\$2state and (enable or disable) and high and low and "any of"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/10 08:25
S963	2	"6067627" and tri\$2state and (enable or disable) and high and low	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/10 08:53
S964	1	"5860106".PN.	USPAT; USOCR	OR	ON	2005/06/10 08:58
S965	2	(Bowling with Stephen) and (configuration\$1) and tri\$2state and (enable or disable) and high and low	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/10 09:00
S966	42	((control with logic) or (configurat\$3 with bit)) and ((tri\$1state same high same low) with disable) and (FET or transistor)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/10 09:06
S967	16	((control with logic) or (configurat\$3 with bit)) and (output with (tri\$1state same high same low) with disable) and (FET or transistor)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/10 10:07
S968	17	"5513140"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/10 10:07
S969	3	((control with logic with bit) with tri\$1state with high with low with (disable or enable))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/10 10:35

S97 0	11	"5548544"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/10 10:34
S97 1	24	((control with logic) with tri\$1state with high with low with (disable or enable))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/10 10:38
S97 2	25	((control\$4 with logic) or (configuration adj1 bit)) with tri\$1state with high with low with (disable or enable))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/10 10:38
S97 3	28	((control\$4 with logic) or (configuration adj1 bit)) with ((tri\$1state same high same low) with (disable or enable)))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/10 10:53
S97 4	0	((tri\$2state adj3 bit) and (high adj3 bit) and (low adj3 bit)) with (enable or disable)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/10 10:42
S97 5	80	((tri\$2state with bit) and (high with bit) and (low with bit)) with (enable or disable)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/10 10:42
S97 6	221	((tri\$2state with bit) and (high with bit) and (low with bit)) with (" not enable" or disable)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/13 15:29
S97 7	42	((tri\$2state with bit) and (high with bit) and (low with bit)) with (("not enable" or disable) and enable)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/10 10:47
S97 8	3	((tri\$2state with bit) with (high with bit) with (low with bit)) with (("not enable" or disable) and enable)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/10 10:44
S97 9	3	((tri\$2state with bit) with (high with bit) with (low with bit)) with ("not enable" or disable)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/10 10:43
S98 0	26	((tri\$2state with bit) and (high with bit) and (low with bit)) with ((("not enable" or disable) and enable) with output)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/10 10:47
S98 1	20	((control\$4 with logic) or (configuration adj1 bit)) with ((tri\$1state same high same low) with (disable or enable) with output))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/10 10:54

S99 0	4931	("any one of" same (tri\$2state and high and low))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/10 10:58
S99 1	0	("any one of" same (tri\$2state and high and low)) and (disable or ("not enable"))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/10 10:58
S99 2	2726	("any one of" same (tri\$2state and high and low)) and (disable or ("not enable") or enable)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/10 10:59
S99 3	1115	("any one of" same (tri\$2state and high and low) with (mode or state)) and (disable or ("not enable") or enable)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/10 10:59
S99 4	527	("any one of" same (tri\$2state and high and low) with (mode or state) with output) and (disable or ("not enable") or enable)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/13 14:12
S99 5	50	("any one of" same (tri\$2state and high and low) with (mode or state) with output with (transistor or FET)) and (disable or ("not enable") or enable)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/13 15:33
S99 6	24	(control\$4 with "any one of" same (tri\$2state and high and low) with (mode or state) with output with (transistor or FET)) and (disable or ("not enable") or enable)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/13 15:30